

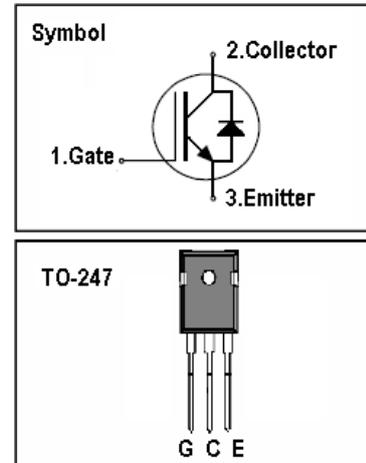


### Features

- 650V 75A,  $V_{CE(sat)(typ.)} = 2.3V$
- Field Stop IGBT Technology
- 10 $\mu$ s Short Circuit Capability
- Square RBSOA
- Positive VCE (on) Temperature Coefficient.

### General Description

LGE's IGBTs offer lower losses and higher energy for application such as motor drive ,UPS, inverter and other soft switching applications.



### Absolute Maximum Ratings

Symbol	Parameter	Value	Units
$V_{CES}$	Collector-Emitter Voltage	650	V
$V_{GES}$	Gate-Emitter Voltage	30	V
$I_C$	Continuous Collector Current ( $T_C=25\text{ }^\circ\text{C}$ )	150	A
	Continuous Collector Current ( $T_C=100\text{ }^\circ\text{C}$ )	75	A
$I_{CM}$	Pulsed Collector Current (Note 1)	300	A
$I_F$	Diode Continuous Forward Current ( $T_C=100\text{ }^\circ\text{C}$ )	60	A
$I_{FM}$	Diode Maximum Forward Current (Note 1)	240	A
$t_{sc}$	Short Circuit Withstand Time	10	us
$I_{sc}$	Short Circuit Current	410	A
$P_D$	Maximum Power Dissipation ( $T_C=25\text{ }^\circ\text{C}$ )	286	W
$P_D$	Maximum Power Dissipation ( $T_C=100\text{ }^\circ\text{C}$ )	71	W
$T_J$	Operating Junction Temperature Range	-55 to +150	$^\circ\text{C}$
$T_{STG}$	Storage Temperature Range	-55 to +150	$^\circ\text{C}$

### Thermal Characteristics

Symbol	Parameter	Max.	Units
$R_{thj-c}$	Thermal Resistance, Junction to case for IGBT	0.35	$^\circ\text{C}/\text{W}$
$R_{thj-c}$	Thermal Resistance, Junction to case for Diode	0.45	$^\circ\text{C}/\text{W}$
$R_{thj-a}$	Thermal Resistance, Junction to Ambient	40	$^\circ\text{C}/\text{W}$



### Electrical Characteristics (T<sub>C</sub>=25°C unless otherwise noted)

Symbol	Parameter	Test Conditions	Min.	Typ.	Max.	Units
BV <sub>CES</sub>	Collector-Emitter Breakdown Voltage	V <sub>GE</sub> = 0V, I <sub>C</sub> = 250uA	650	-	-	V
I <sub>CES</sub>	Collector-Emitter Leakage Current	V <sub>CE</sub> = 600V, V <sub>GE</sub> = 0V	-	-	50	uA
I <sub>GES</sub>	Gate Leakage Current, Forward	V <sub>GE</sub> = 30V, V <sub>CE</sub> = 0V	-	-	200	nA
	Gate Leakage Current, Reverse	V <sub>GE</sub> = -30V, V <sub>CE</sub> = 0V	-	-	-200	nA
V <sub>GE(th)</sub>	Gate Threshold Voltage	V <sub>GE</sub> = V <sub>CE</sub> , I <sub>C</sub> = 0.75mA	4.0	-	6	V
V <sub>CE(sat)</sub>	Collector-Emitter Saturation Voltage	V <sub>GE</sub> = 15V, I <sub>C</sub> = 75A	-	2.3	2.5	V
Q <sub>g</sub>	Total Gate Charge	V <sub>CC</sub> = 400V	-	192	-	nC
Q <sub>ge</sub>	Gate-Emitter Charge	V <sub>GE</sub> = 15V	-	27	-	nC
Q <sub>gc</sub>	Gate-Collector Charge	I <sub>C</sub> = 75A	-	105	-	nC
t <sub>d(on)</sub>	Turn-on Delay Time	V <sub>CC</sub> = 400V V <sub>GE</sub> = 15V I <sub>C</sub> = 75A R <sub>G</sub> = 12Ω Inductive Load T <sub>C</sub> = 25 °C	-	38	-	ns
t <sub>r</sub>	Turn-on Rise Time		-	123	-	ns
t <sub>d(off)</sub>	Turn-off Delay Time		-	183	-	ns
t <sub>f</sub>	Turn-off Fall Time		-	91	-	ns
E <sub>on</sub>	Turn-on Switching Loss		-	3.5	-	mJ
E <sub>off</sub>	Turn-off Switching Loss		-	2.4	-	mJ
C <sub>ies</sub>	Input Capacitance	V <sub>CE</sub> = 25V	-	2.54	-	nF
C <sub>oes</sub>	Output Capacitance	V <sub>GE</sub> = 0V	-	230	-	pF
C <sub>res</sub>	Reverse Transfer Capacitance	f = 1MHz	-	60	-	pF
R <sub>Gint</sub>	Integrated gate resistor	f = 1M; V <sub>pp</sub> = 1V		1.6	-	Ω

### Electrical Characteristics of Diode (T<sub>C</sub>=25°C unless otherwise noted)

Symbol	Parameter	Test Conditions	Min.	Typ.	Max.	Units
V <sub>F</sub>	Diode Forward Voltage	I <sub>F</sub> = 60A	-	1.5	-	V
t <sub>rr</sub>	Diode Reverse Recovery Time	V <sub>CE</sub> = 400V	-	115	-	ns
I <sub>rrm</sub>	Diode peak Reverse Recovery Current	I <sub>F</sub> = 60A	-	16.0	-	A
Q <sub>rr</sub>	Diode Reverse Recovery Charge	dI <sub>F</sub> /dt = 500A/us	-	0.88	-	uC

#### Notes:

1. Repetitive Rating: Pulse width limited by maximum junction temperature



### Typical Performance Characteristics

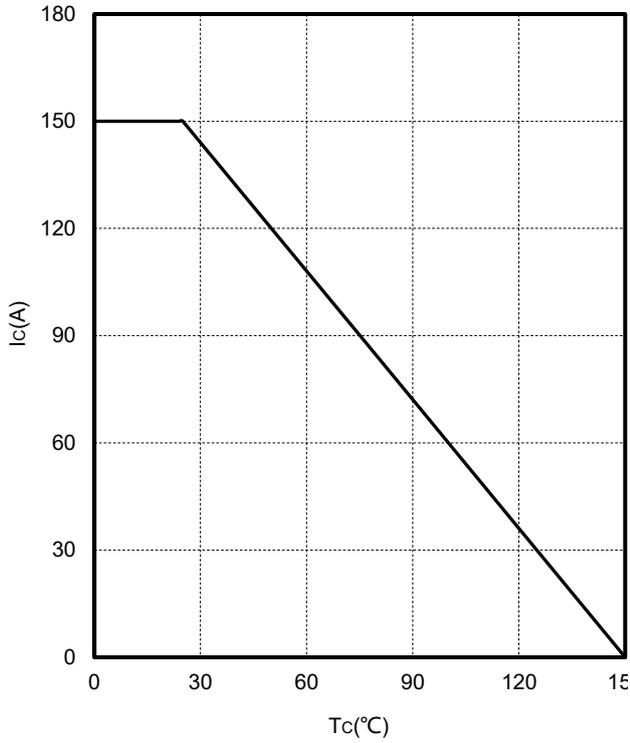


Fig 1. DC Collector current as a function of case temperature ( $V_{GE} \geq 15V$ ,  $T_j \leq 150^\circ C$ )

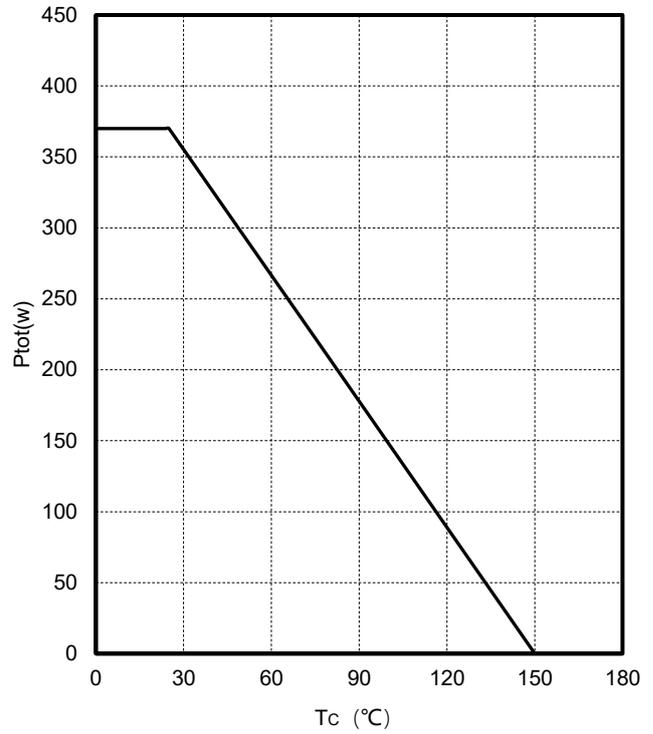


Fig 2. Power dissipation as a function of case temperature ( $T_j \leq 150^\circ C$ )

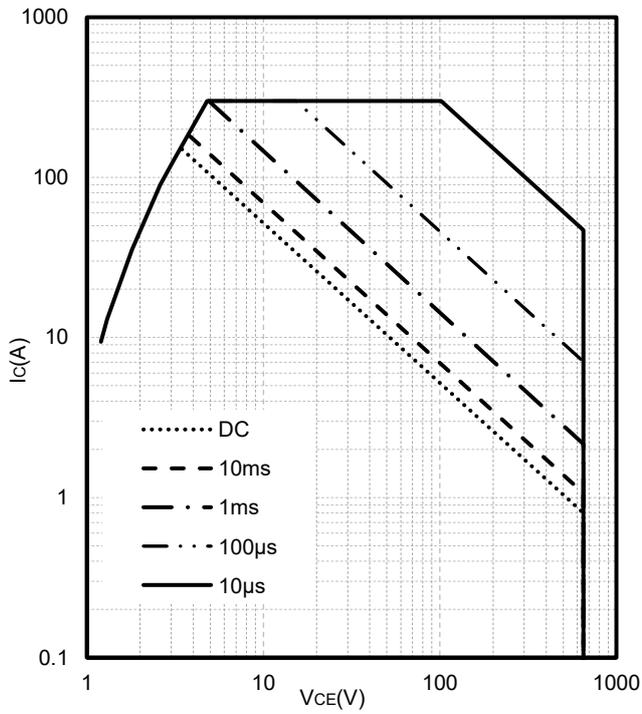


Fig 3. IGBT Forward safe operation area

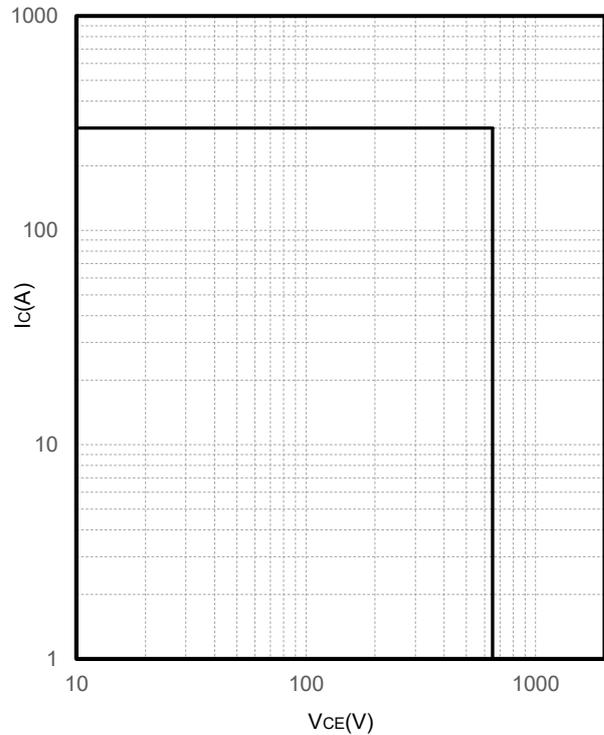


Fig 4. IGBT Reverse safe operation area

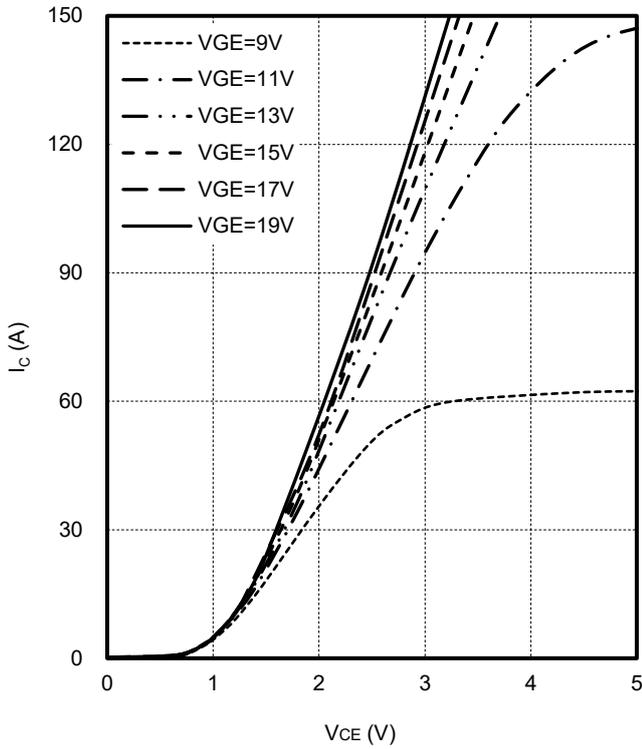


Fig 5. Typical output characteristic (Tj=25°C)

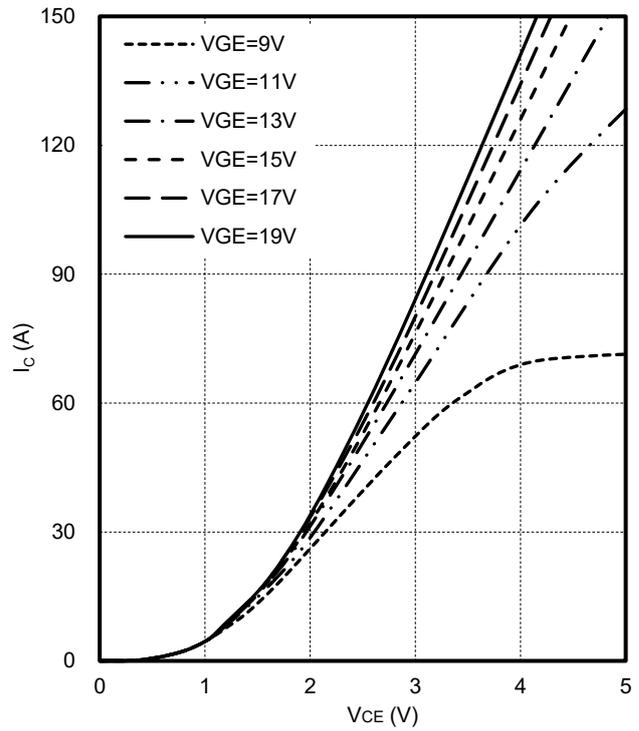


Fig 6. Typical output characteristic (Tj=125°C)

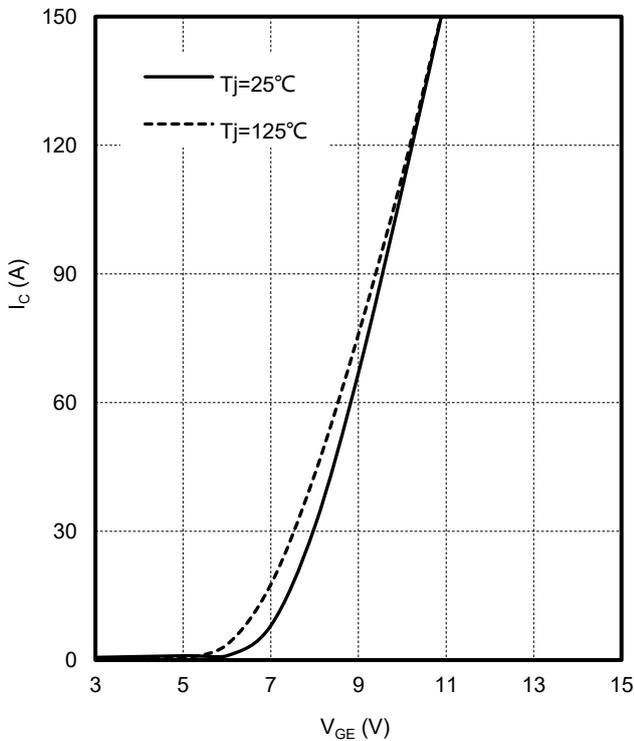


Fig 7. Transfer characteristic IGBT,  
 $I_C = f(V_{GE}), V_{CE} = 20V$

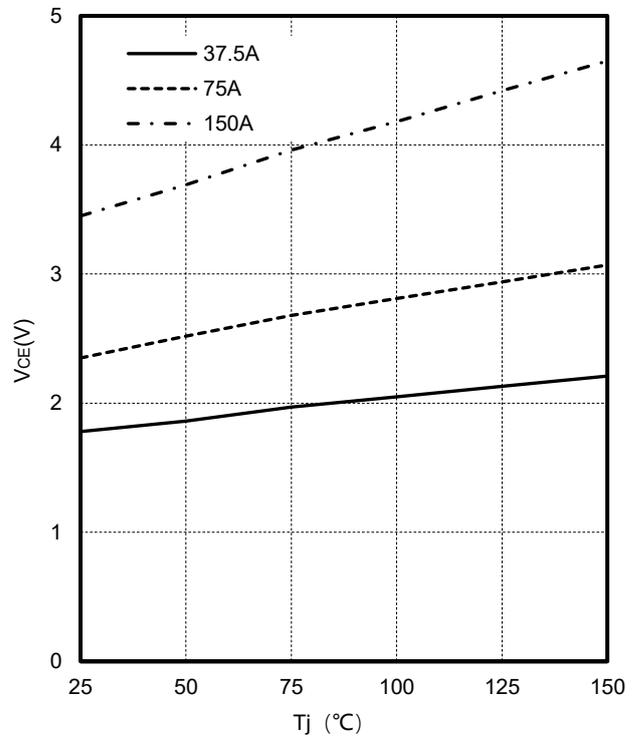


Fig 8. Typical collector-emitter saturation voltage  
as a function of junction temperature,  
 $I_{GBT} V_{CEsat} = f(T_{vj}), V_{GE} = 15 V$

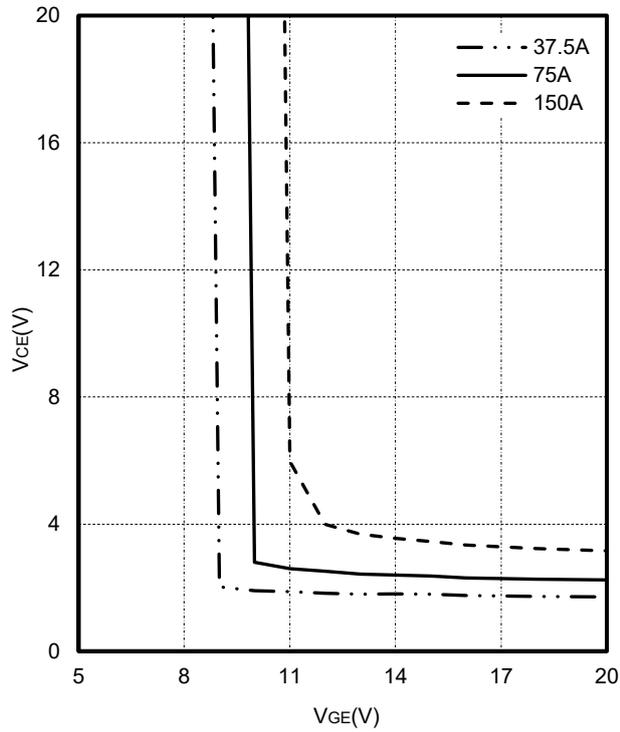


Fig 9. Typical collector-emitter saturation voltage as a function of VGE ( $T_j=25^\circ\text{C}$ )

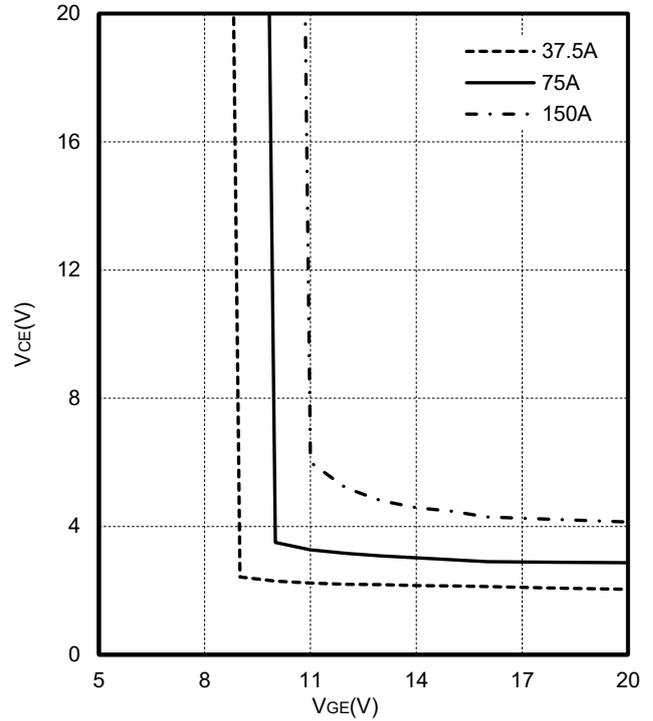


Fig 10. Typical collector-emitter saturation voltage as a function of VGE ( $T_j=125^\circ\text{C}$ )

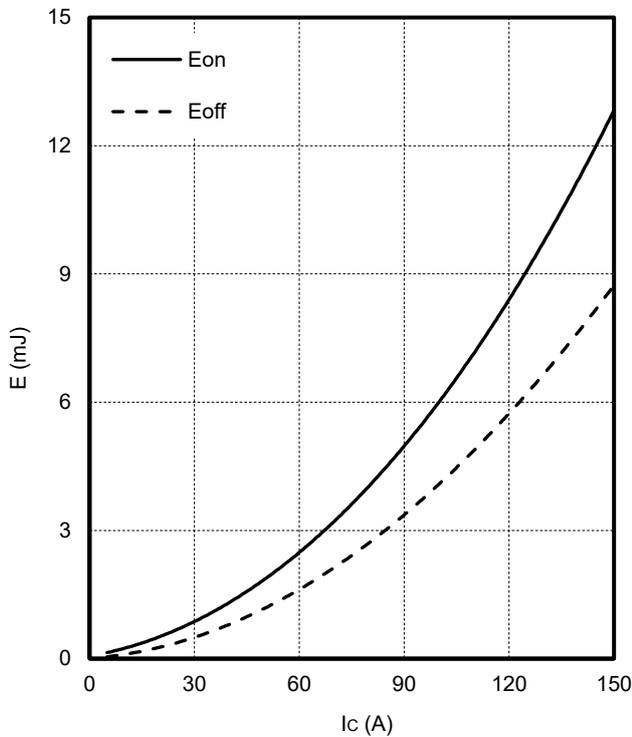


Fig 11. Typical switch energy as a function of  $I_c$  (Inductive load,  $T_j=25^\circ\text{C}$ ,  $V_{ce}=400\text{V}$ ,  $V_{ge}=15\text{V}$ ,  $R_g=12\Omega$ )

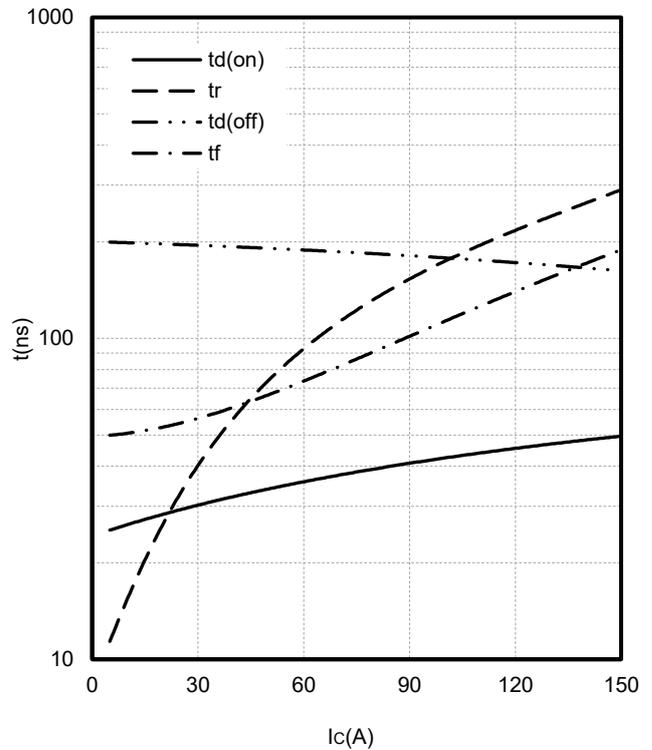


Fig 12. Typical switch time as a function of  $I_c$  (Inductive load,  $T_j=25^\circ\text{C}$ ,  $V_{ce}=400\text{V}$ ,  $V_{ge}=15\text{V}$ ,  $R_g=12\Omega$ )

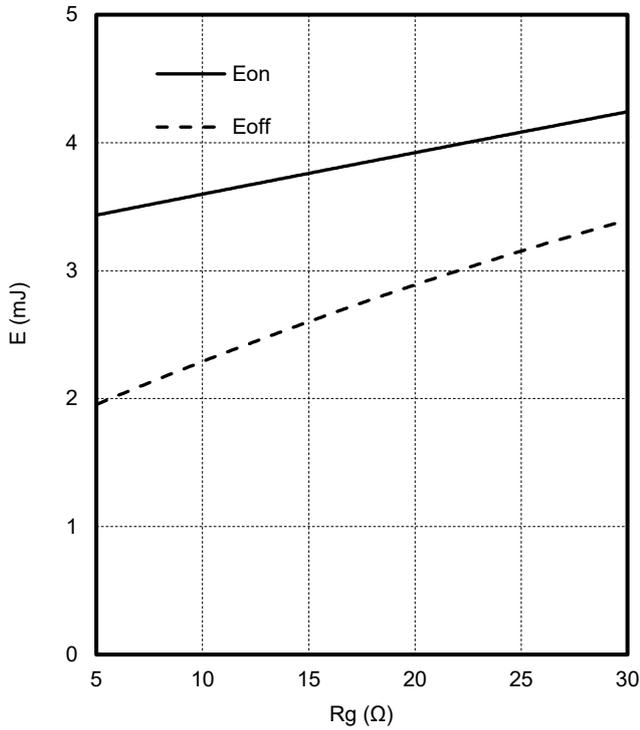


Fig 13. Typical switch energy as a function of Rg (Inductive load,  $T_j=25^\circ\text{C}$ ,  $V_{ce}=400\text{V}$ ,  $V_{ge}=15\text{V}$ ,  $I_c=75\text{A}$ )

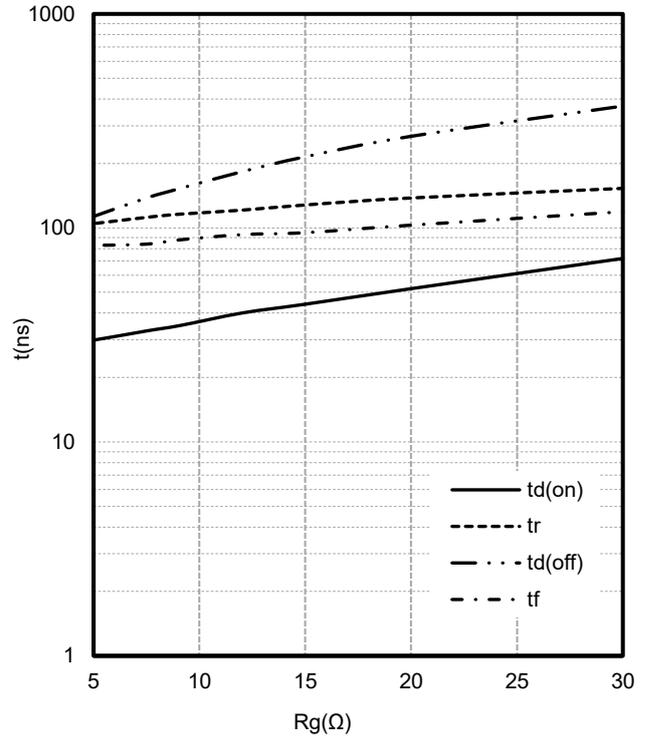


Fig 14. Typical switch time as a function of Rg (Inductive load,  $T_j=25^\circ\text{C}$ ,  $V_{ce}=400\text{V}$ ,  $V_{ge}=15\text{V}$ ,  $I_c=75\text{A}$ )

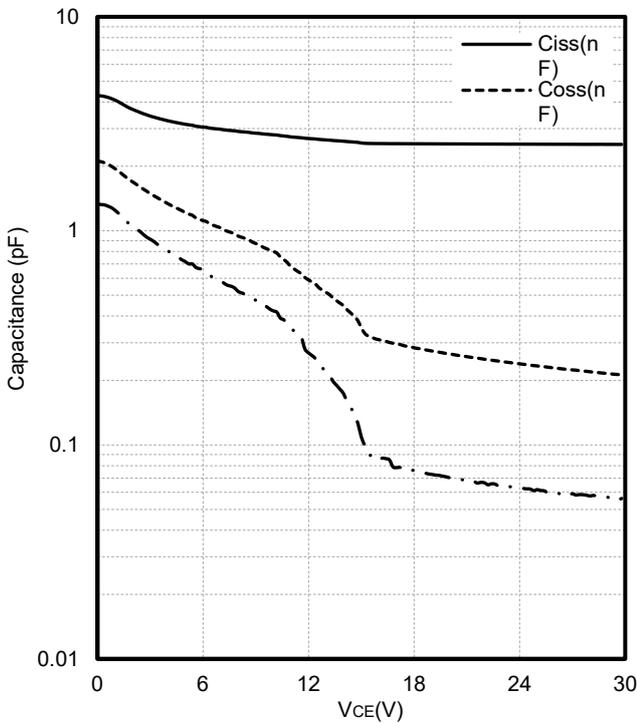


Fig 15. Typical capacitance as a function of collector-emitter voltage ( $V_{ge}=0\text{V}$ ,  $f=1\text{MHz}$ )

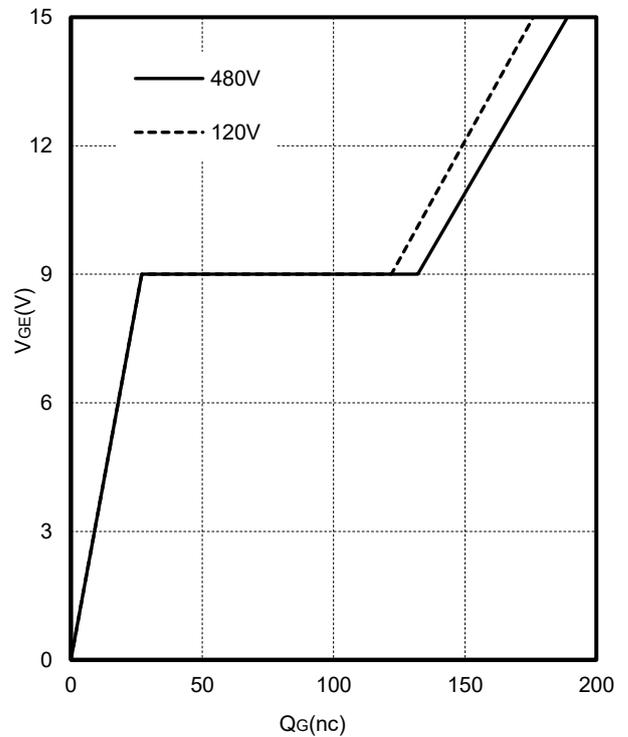


Fig 16. Typical gate charge ( $I_c=75\text{A}$ )

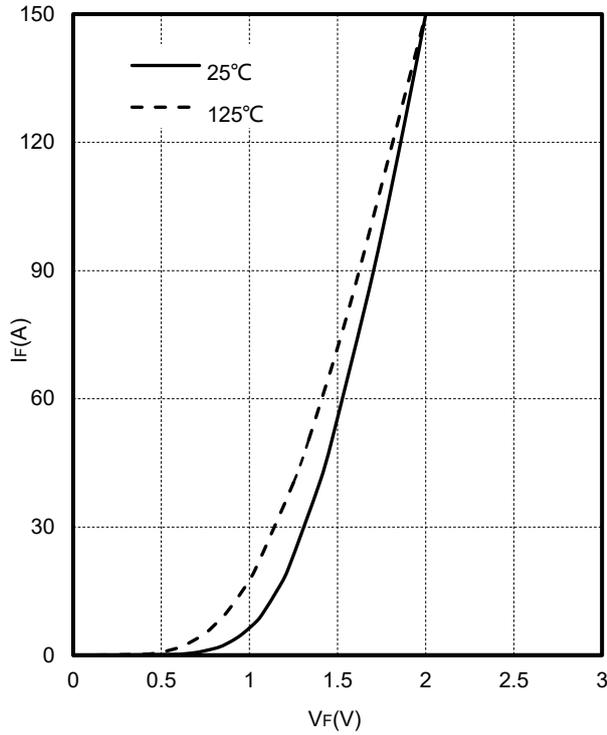


Fig 17. Typical diode forward current as a function of forward voltage

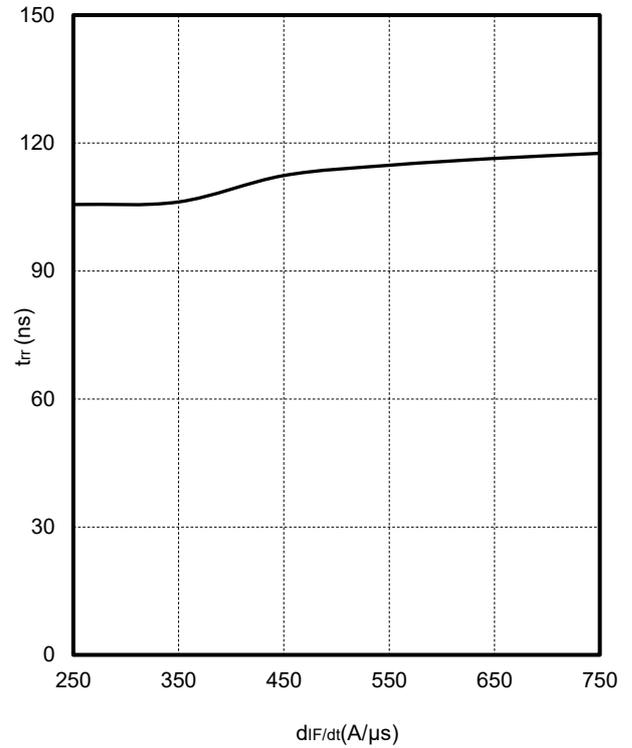


Fig 18. Typical trr as a function of diF/dt, Vr=400V, If=75A, Tj =25°C

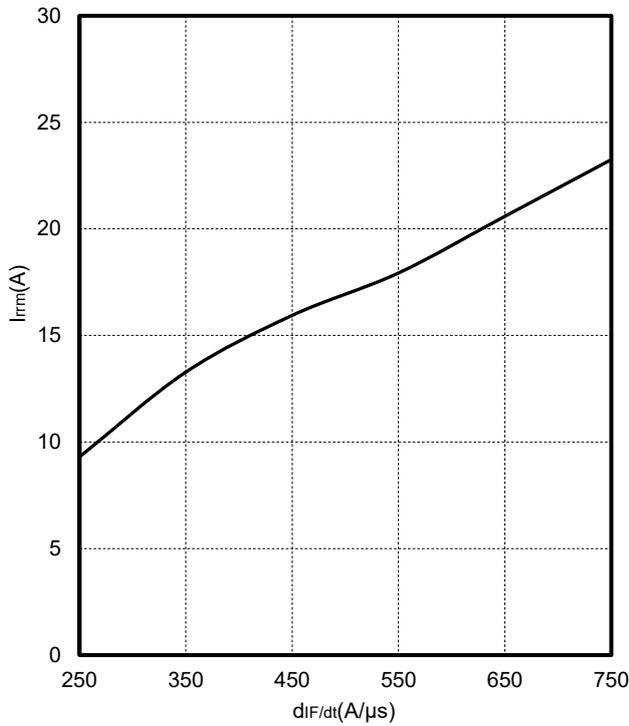


Fig 19. Typical Irrm as a function of diF/dt, Vr=400V, If=75A, Tj =25°C

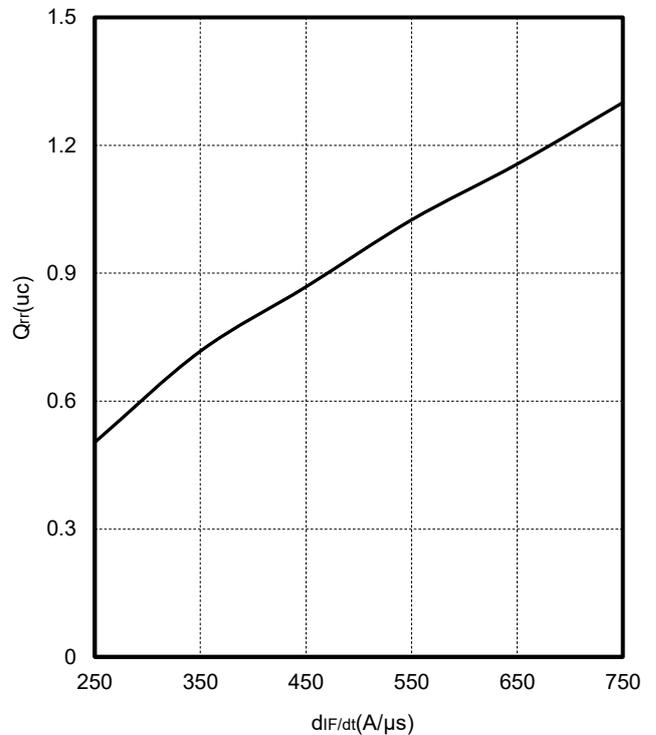


Fig 20. Typical Qrr as a function of diF/dt, Vr=400V, If=75A, Tj =25°C

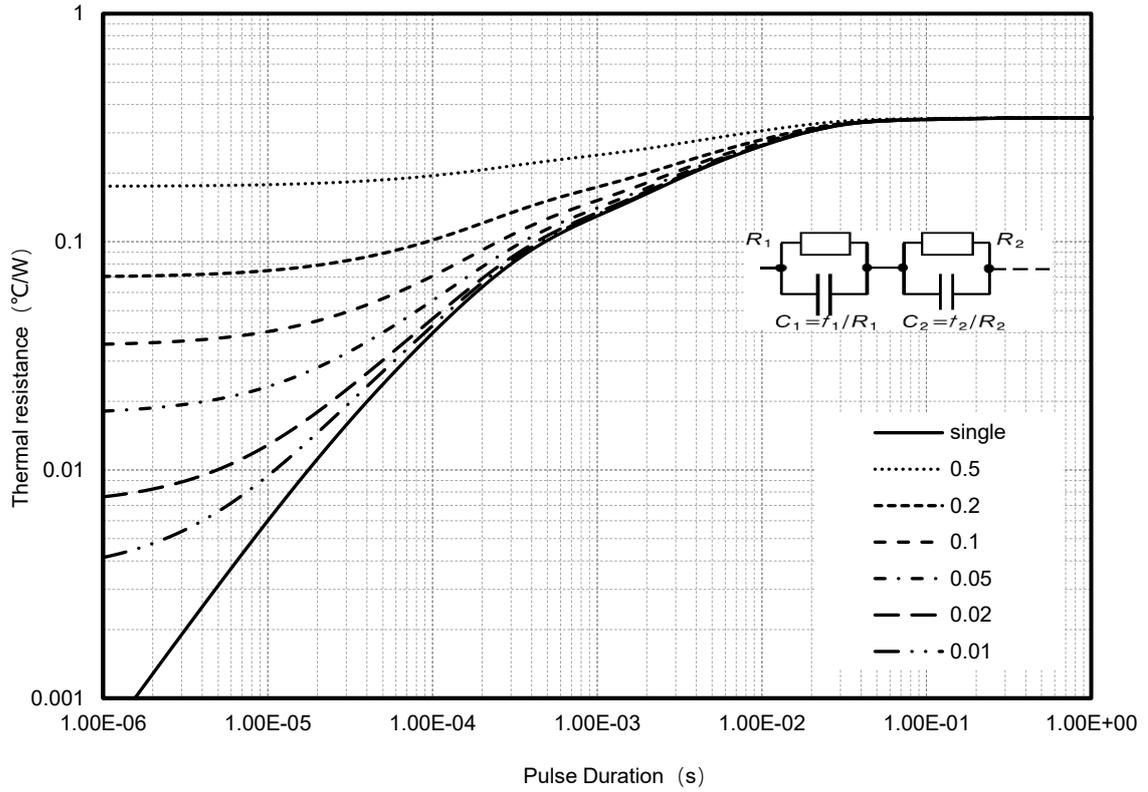
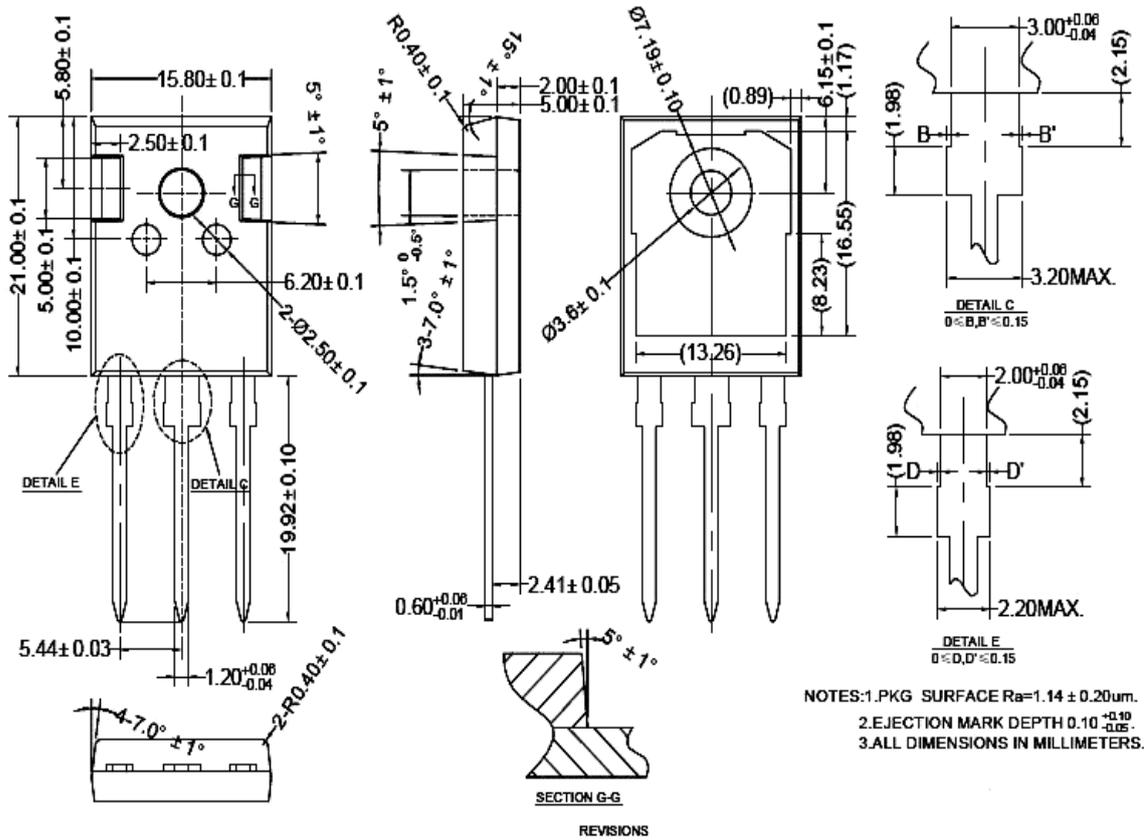


Fig 21. IGBT transient thermal impedance,  $Z_{th}=f(t_p), D=tp/T$



### TO247 PACKAGE OUTLINE



NOTES: 1. PKG SURFACE Ra=1.14 ± 0.20 μm.  
 2. EJECTION MARK DEPTH 0.10  $\begin{matrix} +0.10 \\ -0.05 \end{matrix}$   
 3. ALL DIMENSIONS IN MILLIMETERS.

公差标注	公差值	表面粗糙度
0	±0.2	Ra3.2~6.3
0.0	±0.1	Ra1.6~3.2
0.00	±0.01	Ra0.8~1.6
0.000	±0.005	Ra0.4~0.8
0.0000	±0.002	Ra0.2~0.4

0 ≤ D, D' ≤ 0.15

NOTES: 1. PKG SURFACE Ra=1.14 ± 0.20 μm.  
 2. EJECTION MARK DEPTH 0.10  $\begin{matrix} +0.10 \\ -0.05 \end{matrix}$   
 3. ALL DIMENSIONS IN MILLIMETERS.